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TITLE: INFORMATION PROCESSOR  
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INVENTOR-INFORMATION:

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ABSTRACT:

PROBLEM TO BE SOLVED: To simplify a feeding structure which supplies the power to a logical system unit from a power unit by connecting a power unit and a logical system unit to a power feed terminal to supply the power to the logical system unit from the power unit via a bus bar.

SOLUTION: A front face consists of the PL racks 14 and 15 in an information processor, and plural arithmetic units 1 are mounted on the

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rack 15 with plural  
power units 2 mounted on the rack 14 respectively. A back  
face consists of a  
PL rack 5 where plural I/O units 30 and I/O power units 31  
and an electronic  
device substrate 8 are mounted. Then a logical PL 3 and a  
power PL 4 are  
attached between the racks 14 and 15 of the front face and  
the rack 5 of the  
back face respectively. The signal connectors 9 are  
provided on both sides of  
the PL 3 and connected to the units 1, units 30 and the  
substrate 8. The feed  
connectors 10 provided on the front face of the PL 4 are  
divided into the  
connectors which are connected to the units 1 and the  
connectors which are  
connected to the units 2. These connectors are connected  
together via a bus  
bar 6.

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